

DEC1000R5

Color Compressed Video Decoder

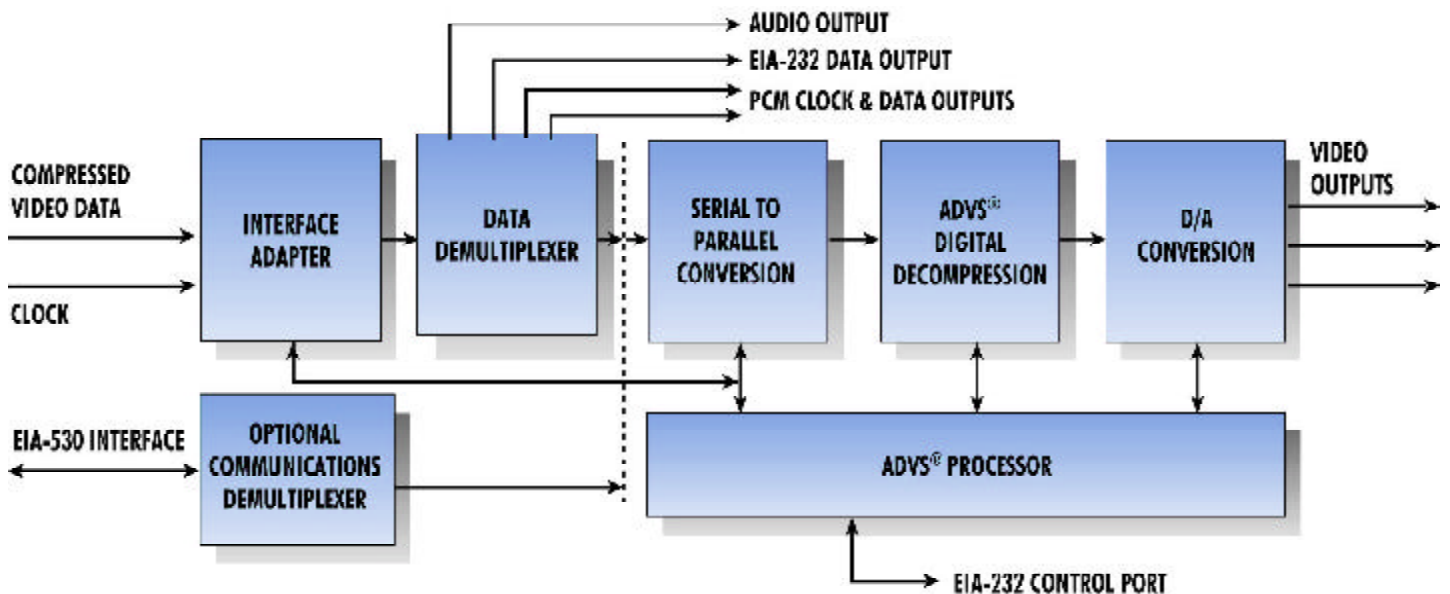
Features

- **ADVS[®] - Adaptive Digital Video Standard Compression Algorithm**
- **Real-Time Digital Compression of Full-Motion Video**
- **Standard 19-inch Rack Mount Chassis**
- **Low Power - 25 Watts Typical at 120 VAC**
- **No Blurred Motion - Each Frame Separately Displayed**
- **User Programmable Video Parameters via EIA-232 Port**
- **Composite NTSC Color and PAL, Y/C Capabilities**
- **Receives at Rates Up to 10 Mbps Over Any Digital Medium**

The decoder receives digitized video signals from the encoder via any type of digital communications link. The decoder accepts the digital video signal from the communications link devices in a digital data and digital clock format, and then converts these signals back to the original standard NTSC color analog television picture. After performing the required decoding functions, the Model DEC1000R5 outputs a standard NTSC color video signal suitable for either recording or viewing on a color television monitor. The color video compression decoder has the capability of being connected into the system at any time from the beginning of the session. The system operating parameters are automatically derived from information contained within the compressed video data stream. No handshake with the encoder is required, because the decoder will output the first full picture received. The decoder is capable of achieving data synchronization upon presentation of the data and clock signals. The data demultiplexer or optional communications demultiplexer allows restoration of data from a similarly equipped encoder.

DEC1000R5

DEC1000R5 Decoder Functional Block Diagram



DEC1000R5

Color Compressed Video Decoder

Standard Functions

| FEATURE | DESCRIPTION |
|-----------------------------|--|
| Video Outputs * | Three outputs of composite or composite Y/C NTSC or PAL, 1 Vp-p, 75 ohms |
| Digital Data Inputs | NRZL clock and data, TTL or EIA-422A levels |
| Communications Port * | EIA-232C at 9600, 4800, 2400, 1200 or 300 baud |
| Data Multiplexer | |
| PCM output * | TTL, CMOS or EIA-422A selectable output levels, maximum data rate is 49% of total link frequency |
| Audio output * | 300 Hz to 3000 Hz, 600 ohms, 1 Vp-p |
| EIA-232 multiplex output * | EIA-232C at 9600, 4800, 2400, 1200 or 300 baud |
| Resolution * | 560, 280 or 140 pixels per line |
| Quantization * | 240 steps |
| Cropping * | 4 steps |
| Compression Standard | ADVS® |
| Operating Frequencies | Any rate up to 10 Mbps |
| Interface Connectors | BNC for each video output, BNC for clock and data inputs, DB25 for EIA-232 interface, IEC type detachable power cord |
| Input Power | 95-265 VAC, 50 Hz or 60 Hz |
| Power Consumption | 30 watts maximum |
| Dimensions | 19-inch rack, 5.25 in. high, 12 in. deep |
| Weight | 11.7 pounds |
| Operating Temperature Range | -20 to +70°C |
| Video Insertion Generator * | Upper and lower case text, on-screen time and data, miniature color bar insertion |
| Pattern Generator * | Color bars, SMPTE bars, and solid fields |
| MIL-STD-810 Testing | Consult factory for details |
| MIL-STD-461 Testing | Consult factory for details |

Optional Functions

| FEATURE | DESCRIPTION |
|------------------------------------|---|
| Forward Error Correction * | Reed-Solomon correction at 12.3% overhead |
| EIA-530 Interface | Allows connection to T1, 56Kb, and fractional rate modems |
| Communications Multiplexer * | Allows inband quad EIA-232C control and inband audio multiplexing |
| T1 CSU/DSU * | Internal T1 (1.544 Mbps) line interface (Optional) |
| Dual T1 CSU/DSU * | Internal Dual T1 (3.088 Mbps) line interface (Optional) |
| DBS12 Multi-Freq. Bit Synchronizer | Recovers clock and data from analog receiver data output |
| BRC1000 PCM Converter | Converts decoder burst PCM output to continuous PCM output |

*Software Selectable

